

IP Name:	mixed mode DLL
Grade:	Verification
Process:	0.25um
Foundry:	TSMC
Features:	2.2~3.3V mixed mode Static Phase Error@100MHz, 2.7V 12.4ps
Release Notes:	Operating Frequency 75MHz Lock Time@100MHz, 2.7V < 135ns (digital) Static Phase Error@100MHz, 2.7V 12.4ps
Supply Voltage (Min)	2.2V
Supply Voltage (Max)	3.3V
Output Frequency (Min)	70MHz
Output Frequency (Max)	150MHz
Power Consumption (Min)	15.8 mW
Power Consumption (Max)	47.2mW
Clock Jitter (Max)	12.4ps
Die Size	0.737mm ²
Aspect Ratio	0.61

